

JC832 U.S. PTO

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## U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

(1)

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10017031	10/30/2001	257	724	281	S. Clark

\*\*APPLICANTS: Kiss David;

2815

\*\*CONTINUING DATA VERIFIED:

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\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	INTO-0011-US (P13281)
Verified and Acknowledged Examiners' initials	
TITLE: Packaged combination memory for electronic devices	
U.S. DEPT. OF COMM./PAT. & TM.-PTO-4361 (Rev. 12-94)	

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
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